

Honeywell's Docket No. H0001800 DIV (4960)
Practitioner's Docket No. 100665.0024US2

COPY OF PAPERS
ORIGINALLY FILED

#3 2841
09/25/02
PATENT



IN THE UNITED STATE PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231

In re application of: **Yutaka DOI**

Serial No: 10/029,778 ✓

Filed: **October 18, 2001**

For: **Layered Circuit Boards and
Methods of Production Thereof**

Group: **2841**

Examiner: **Not Yet Assigned**

RECEIVED
SEP 17 2002
TC 2800 MAIL ROOM

Box DD
Assistant Commissioner for Patents
Washington, D.C. 20231

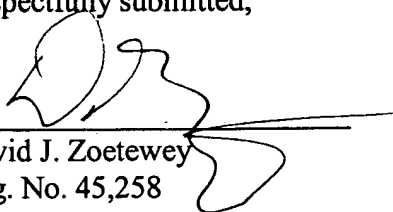
**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
WITHIN THREE MONTHS OF FILING OR
BEFORE MAILING OF FIRST OFFICE ACTION (37 C.F.R. 1.97(b))
IDENTIFICATION OF TIME OF FILING THE ACCOMPANYING
INFORMATION DISCLOSURE STATEMENT**

The information disclosure statement submitted herewith is being filed within three months of the filing date of the application or date of entry into the national stage of an international application or before the mailing date of a first Office action on the merits, whichever event occurs last. 37 C.F.R. 1.97(b).

Respectfully submitted,

Date: September 9, 2002

By:


David J. Zoetewey
Reg. No. 45,258

RECEIVED

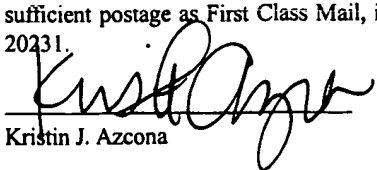
SEP 23 2002

TECHNOLOGY CENTER H3/00

Attorneys for Applicant
Rutan & Tucker, LLP
611 Anton Blvd., 14th Floor
Costa Mesa, CA 92835
Tel: (714) 641-5100
Fax: (714) 546-9035

CERTIFICATE OF MAILING (37 C.F.R. 1.8(a))

I hereby certify that, on the date shown below, this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.


Kristin J. Azcona

Date: September 9, 2002

COPY OF PAPERS
ORIGINALLY FILED

Honeywell's Docket No. H0001800 DIV (4960)
Practitioner's Docket No. 100665.0024US2

RECEIVED

SEP 17 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
WASHINGTON, D.C. 20231

In re application of: **Yutaka DOI**

Serial No: **10/029,778**

Filed: **October 18, 2001**

For: **Layered Circuit Boards and
Methods of Production Thereof**

Group: **2841**

Examiner: **Not Yet Assigned**

INFORMATION DISCLOSURE STATEMENT

Box DD
Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure imposed by 37 C.F.R. § 1.56 to inform the United States Patent and Trademark Office of all references coming to the attention of the Applicant(s) or attorneys or agents for Applicant(s) which are or may be material to the examination of the subject application, attorneys for the Applicant(s) hereby invite the Examiner's attention to the references listed in the accompanying PTO Form 1449 entitled "List of References Cited".

This submission is understood to complement the results of the Examiner's own independent search. The submission of this Disclosure Statement should not be construed as a representation that a search was made, or that the cited items are inclusive of all relevant and material citations that may be available publicly.

RECEIVED

SEP 23 2002

TECHNOLOGY CENTER R3700

COPY OF PAPERS
ORIGINALLY FILED

Honeywell's Docket No. H0001800 DIV (4960)

Practitioner's Docket No. 100665.0024US2

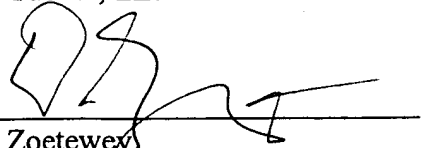


Applicant(s) respectfully request that the Examiner review the foregoing references, as set forth in the Form PTO-1449, and that they be made of record in the file history of the above-captioned application.

Respectfully submitted,

Rutan & Tucker, LLP

Dated: September 9, 2002

By: 
David J. Zoetewey
Reg. No. 45,258

Attorneys for Applicant(s)
611 Anton Blvd., 14th Floor
Costa Mesa, CA 92626
Tel: 714-641-5100
Fax: 714-546-9035

RECEIVED
SEP 23 2002
TECHNOLOGY CENTER R3700

RECEIVED
SEP 23 2002
TECHNOLOG